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and hence $1.5 \times n$ MIPS instructions. Computer A operates at 2.5 GHz, i.e. it takes 0.4ns per clock. So

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$$200+15+10+70+20=315 \text{ per second}$$
$$200 + 15 + 10 + 70 + 20 =$$

315 per second .
Verified Answer. The critical path is the path which is the longest path for the latency . It is the latency of the instruction which is necessary for the execution of the instruction. Here is a tip:

[Solved] Chapter 4, Problem 4.4 - Computer Organization

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