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Oxford University Publishing Microelectronic Circuits by Adel oxS. Sedra and Kenneth C. Smith (0195323033) 5.1. Device Structure and Operation

Figure 5.1: Physical structure of the enhancement-type NMOS transistor: (a) perspective view, (b) cross- section. Note that typically $L = 0.03\mu\text{m}$ to

$1\mu\text{m}$, $W = 0.1\mu\text{m}$ to $100\mu\text{m}$, and the thickness of the oxide

Chapter #5: MOSFET's

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